

Resistor-Programmable Temperature Switch

1 Features

- Threshold Accuracy:
 - ±0.5°C Typical
 - ±3°C Maximum (60°C to 100°C)
- Temperature Threshold Set By 1% External Resistor
- Low Quiescent Current: 33µA Typical
- · Open-Drain, Active-Low Output Stage
- Pin-Selectable 30°C or 10°C Hysteresis
- Reset Operation Specified at V_{CC} = 0.8V
- Supply Range: 2.7V to 5.5V
- Package: 5-Pin SOT-23, 6-Pin DFN6L

2 Applications

- Computers (Laptops and Desktops)
- Servers
- Industrial and Medical Equipment
- Storage Area Networks
- Automotive

3 Description

The GD30TS708N is a fully-integrated, resistor-programmable temperature switch with a temperature threshold that is set by just one external resistor within the entire operating range. The GD30TS708N provides an open-drain, active-low output and has a 2.7V to 5.5V supply-voltage range.

The temperature threshold accuracy is typically $\pm 0.5^{\circ}$ C, with a maximum of $\pm 3^{\circ}$ C (60°C to 100°C). The quiescent current consumption is typically 33 μ A. Hysteresis is pinselectable to 30°C or 10°C.

The GD30TS708N is available in a 5-pin, SOT23 package and 6-pin, DFN6L package.

Device Information¹

PART NUMBER	PACKAGE	BODY SIZE (NOM)
GD30TS708N	SOT23-5	2.90mm × 1.60mm
GD3013706N	DFN6L	1.50mm × 1.50mm

1. For packaging details, see *Package Information* section.

Typical Application Schematic

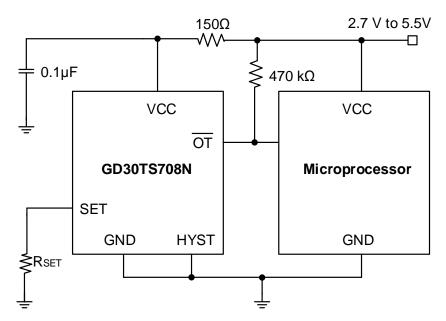




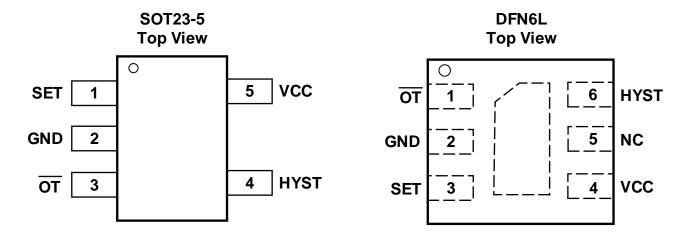
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4 Device Overview

4.1 Pinout and Pin Assignment



4.2 Pin Description

	PINS		PIN	FUNCTION
NAME	SOT23-5	DFN6L	TYPE ¹	FUNCTION
SET	4	3		Temperature set point. Connect an external 1% resistor between SET
SEI	ı	3	ı	and GND.
GND	2	2	G	Device ground.
ŌT	3	1	0	Open-drain, active low output.
HYST	4	6	I	Hysteresis selection. For 10°C, HYST = VCC; for 30°C, HYST = GND.
VCC	5	4	Р	Power-supply voltage (2.7 V to 5.5 V)
NC		5		Not Connect.

^{1.} P = power, G = Ground, I = input, O = Output.



5 Parameter Information

5.1 Absolute Maximum Ratings

Exceeding the operating temperature range (unless otherwise noted)¹

SYMBOL	PARAMETER	MIN	MAX	UNIT
Vcc	Power supply	-0.3	6	V
Vот	Output Voltage at OT	-0.3	6	V
V _{IO}	Intput Voltage at SET and HYST	-0.3	V _{CC} +0.3	V
TA	Operating temperature	-40	125	°C
TJ	Junction temperature		150	°C
T _{stg}	Storage temperature	-65	150	°C

^{1.} Over operating free-air temperature range (unless otherwise noted). Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device.

5.2 Recommended Operation Conditions

SYMBOL ¹	PARAMETER	MIN	TYP	MAX	UNIT
Vcc	Supply voltage	2.7	3.3	5.5	V
TA	Operating Temperature Range	0		125	°C

^{1.} Unless otherwise stated, over operating free-air temperature range.

5.3 Electrical Sensitivity

SYMBOL ¹	CONDITIONS	VALUE	UNIT
Vesd(HBM)	Human Body Mode (HBM), per ANSI/ESDA/JEDEC JS-001	±5000	V
V _{ESD(CDM)}	Charge-device model (CDM), per ANSI/ESDA/JEDEC JS-002-20222	±2000	V
V _{ESD(MM)}	Machine Mode (MM), per JEDEC-STD Classification	200	V
LU	Latch up	100	mA

1. Unless otherwise stated, over operating free-air temperature range.

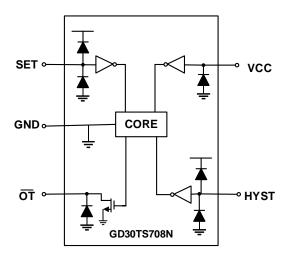


Figure 1. Equivalent Internal ESD Circuitry



5.4 Thermal Resistance

SYMBOL ¹	CONDITIONS	PACKAGE	VALUE	UNIT
ΘЈА	Natural convection, 2S2P PCB		217.9	°C/W
ΘЈВ	Cold plate, 2S2P PCB		44.6	°C/W
Θ _{JC}	Cold plate, 2S2P PCB	SOT23-5	86.3	°C/W
Ψ_{JB}	Natural convection, 2S2P PCB		43.8	°C/W
Ψ _{JT}	Natural convection, 2S2P PCB		4.4	°C/W

^{1.} Thermal characteristics are based on simulation, and meet JEDEC document JESD51-7.

5.5 Electrical Characteristics

Electrical characteristics of devices at T_A = +25°C and V+ = 1.4V to 3.6V, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
POWER SU	PPLY		•			
Vcc	Power Supply Voltage		2.7		5.5	V
_	Cumply Current	Vcc=5V		33	55	μΑ
Icc	Supply Current	Vcc=2.7V		33	55	μΑ
TEMPERAT	URE					
TA	Operating Temperature Range		0		125	°C
TERROR	Accuracy (Temperature Error)	T _A =+60°C to +100°C		±0.5	±3	°C
DIGITAL IN	PUT (HYST)					
VIH	High-level input voltage		0.7 x Vcc			V
VIL	Low-level input voltage				0.3 x Vcc	V
Cin	Input capacitance			10		pF
ANALOG IN	IPUT (SET)					
VIN	Input voltage range		0		Vcc	V
I _{lkg_in}	Input leakage current			1		μΑ
DIGITAL OF	PEN-DRAIN OUTPUT (OT)					
IOT_SINK	Output sink current	$V_{OT} = 0.3V$	5	12		mA
I _{OT_lkg}	Output leakage current	V _{OT} = V _{CC}		1		μΑ



6 Functional Description

6.1 Overview

The GD30TS708N is a fully-integrated, resistor-programmable temperature switch that incorporates two temperature-dependent voltage references and one comparator. One voltage reference exhibits a positive temperature coefficient (tempco), and the other voltage reference exhibits a negative tempco. The temperature at which both voltage references are equal determines the temperature trip point.

The GD30TS708N temperature threshold is programmable from 0°C to 125°C and is set by an external 1% resistor from the SET pin to the GND pin. The GD30TS708N has an open-drain, active-low output structure that easily interfaces with a microprocessor.

6.2 Hysteresis Input

The HYST pin is a digital input that allows the input hysteresis to be set at either 10°C (when HYST = V_{CC}) or 30°C (when HYST = GND). The hysteresis function keeps the \overline{OT} pin from oscillating when the temperature is near the threshold. Thus, always connect the HYST pin to either VCC or GND. Other input voltages on this pin can cause abnormal supply currents or a device malfunction.



7 Application Information

7.1 Typical Application Circuit

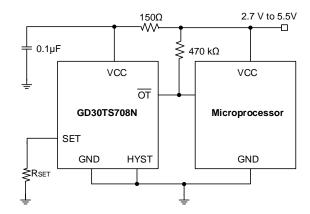


Figure 2. Typical Connections of the GD30TS708N

7.2 Set-Point Resistor(Rset)

Set the temperature threshold by connecting R_{SET} from the SET pin to GND. The value of R_{SET} is determined using either Equation(1):

$$R_{SET}(k\Omega) = 0.0012T^2 - 0.9308T + 96.147$$
 (1)

where:

T = temperature threshold in degrees Celsius.

7.3 Thermal Considerations

The GD30TS708N quiescent current is typically $33\mu A_{\circ}$. The device dissipates negligible power when the output drives a high-impedance load. Thus, the die temperature is the same as the package temperature. In order to maintain accurate temperature monitoring, provide a good thermal contact between the GD30TS708N package and the device being monitored. The rise in die temperature as a result of self-heating is given by Equation(2):

$$\Delta T_{J} = P_{DISS} \times \theta_{JA} \tag{2}$$

where:

 P_{DISS} = power dissipated by the device.

 θ_{JA} = package thermal resistance. Typical thermal resistance for SOT-23 package is 217.9°C/W.

To limit the effects of self-heating, keep the output current at a minimum level.

7.4 Power Supply Recommendations

The GD30TS708N low supply current and supply range allow this device to be powered from many sources. Any significant noise on the V_{CC} pin can result in a trip-point error. Minimize this noise by low-pass filtering the device supply (V_{CC}) using a 150 Ω resistor and a 0.1 μ F capacitor.



7.5 Typical Application Curves

 $T_A = 25$ °C and $V_{CC}=2.7V$ to 5.5V, unless otherwise noted.

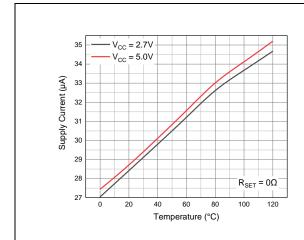


Figure 3. Supply Current vs Temperature

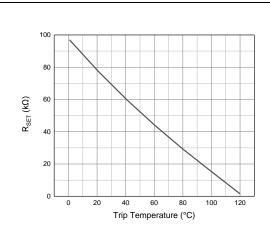


Figure 4. R_{SET} vs Trip Temperature

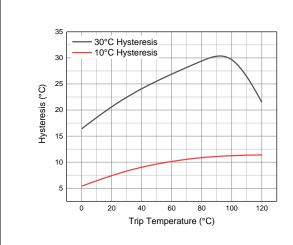


Figure 5. Hysteresis vs Trip Temperature

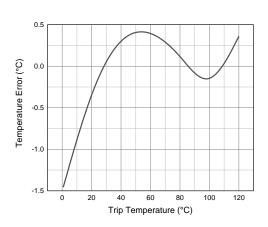


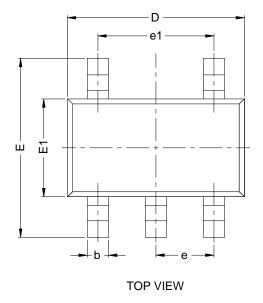
Figure 6. Temperature Error vs Trip Temperature

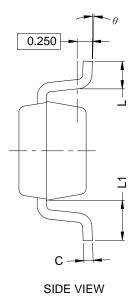


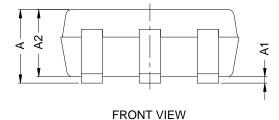
8 Package Information

8.1 Outline Dimensions

SOT23-5 Package Outline







NOTES:

- 1. All dimensions are in millimeters.
- 2. Package dimensions does not include mold flash, protrusions, or gate burrs.
- 3. Refer to the Table 1. SOT23-5 dimensions(mm).



Table 1. SOT23-5 dimensions(mm)

SYMBOL	MIN	NOM	MAX	
А	1.05	1.15	1.25	
A1	0.00	0.05	0.10	
A2	1.05	1.10	1.15	
b	0.30	0.40	0.50	
С	0.10	0.15	0.20	
D	2.82	2.92	3.02	
E1	1.50	1.60	1.70	
E	2.65	2.80	2.95	
е		0.950(BSC)		
e1	1.80	1.90	2.00	
L	0.30	0.45	0.60	
L1	0.60REF			
θ	0°		8°	



DFN6L Package Outline ____Cleee C // ccc C ──C SEATING PLANE D PIN 1 CORNER E TOP VIEW SIDE VIEW ◆ D2 → EXPOSED DIE ATTACH PAD е E2 ♦ fff CAB -6X b ♦bbb@CAB PIN 1 I.D. 6X (k)-BOTTOM VIEW

NOTES:

- 1. All dimensions are in millimeters.
- 2. Package dimensions does not include mold flash, protrusions, or gate burrs.
- 3. Refer to the Table 2. DFN6L dimensions(mm).



Table 2. DFN6L dimensions(mm)

SYMBOL	MIN	NOM	MAX		
А	0.5	0.55	0.6		
A1	0	0.02	0.05		
A2		0.40			
A3		0.152 REF			
b	0.2	0.25	0.3		
D	1.5 BSC				
Е		1.5 BSC			
е		0.5 BSC			
D2	0.36	0.46	0.56		
E2	0.9	1.0	1.1		
L	0.20	0.25	0.30		
K	0.27 REF				



9 Ordering Information

Ordering Code	Package Type	ECO Plan	Packing Type	MOQ	OP Temp(°C)
GD30TS708NNSTR-I	SOT23-5	Green	Tape & Reel	3000	0°C to +125°C
GD30TS708NSETR-I	DFN6L	Green	Tape & Reel	4000	0°C to +125°C



10 Revision History

REVISION NUMBER	DESCRIPTION	DATE
1.0	Initial release and device details	2024



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